## **RESUME**



Name : Ashok Kumar Rajput Father's Name : Shri Ganga Saran

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Marital Status : Married

Nationality : Indian

## **Educational Qualification:**

# 1. Non Technical:

S. NO.	Standard	Standard Board / University		Division
1.	High school	U.P. Board	67%	First
2.	Intermediate	U.P. Board	67%	First

# 2. Technical:

S.No.	Discipline/Degree	Collage/Uni.	Year of	%	Division
			passing	marks	
1.	Electrical Engg.	I.E.T. LKO	1998	73 %	First
	B. Tech.	Lucknow			
2.	Instrumentation &	NITTTR,Chandigarh/	2011	71%	First
	Control / M.E	Punjab Uni.			
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# **Experience: More Than 14 years**

S. NO.	Designation	Duration	Institute	Years(appr.)
1.	Lecturer	Aug1998-July2000	M.I.E.T. Meerut	1.5
2.	Lecturer	July2000-March 2005	C.E.R.T. Meerut	5.5
3.	Lecturer	11/03/0528/2/07	R.G.E.C. Meerut	2.0
4.	Sr. Lecturer	01/03/07-30/06/09	R.G.E.C. Meerut	2.0

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	5	AP	30/06/09-31/01/2013	R.G.E.C. Meerut	3.5	
	6	Asso. Prof.	01/02/2013	R.G.E.C. Meerut		

ID: HFNS-1242-Rainut

#### Others:

#### \*\*\*\* As a Author

- 1. Author of Modern Electrical Engg. (Degree level).
- 2. Author of Basic Electrical Engg (Diploma level).
- 3. Author of Electrical Engg II (Diploma level ).
- 4. Author of Electrical M/C I,II (Diploma level ).
- 5. Author of Elementary Workshop(Diploma Level)
- 6. Author of Estimating and costing(Diploma Level)
- 7. Co-Author of Electrical Technology.
- 8. Co-Author of E.M.R.
- 9. Co-Author of Estimating and costing-I (Rajastan Technical Board)
- 10. Co-Author of Estimating and costing-II (Rajastan Technical Board)
- 11. Author of fundamental of Electrical Engg.
- 12. Co-Author of Utilazation of Electrical Engg.
- 13. Co-Author of Electrical Power II
- 14. Co-Author of Control of Electrical Machines
- 15. Co-Author of General Engg.
- 16. Co-Author of Electrical Machines
- 17. Co-Author of Electrical & Electronics Engg. Materials
- 18. Co-Author of Element of Electrical & Electronics Engg.
- 19. Co-Author of Electrical Design & Drawing-I
- 20. Co-Author of Electrical Design & Drawing-II
- 21. Co-Author of E.M.M.

### Other Work Experience/Achievement:

- \*\* Worked as Member of Flying Squad in U.P.T.U End Semester Examination.
- \*\* Worked as a Observer in UPSEE.
- \*\* Worked as a ACS in University Examination.
- \*\* Received Best Teaching Award in 2006 (Teacher's Day) in Radha Govind

Engineering College , Meerut

- \*\*GATE 2000 Qualified.
- \*\*GATE 2007 Qualified.

- \*\*GATE 2008 Qualified.
- \*\* GATE 2009 Qualified
- \*\* GATE 2010 Qualified
- \*\* GATE 2011 Qualified
- \*\* joined in IIT, Delhi as a Jct. Tech. Supdt. In Electrical Engg. Deptt. And left
- \*\* Selected as a Jr.. Engineer in DSSB.

#### **Research Interests:**

- 1. Non Conventional Resources
- 2. Modeling and simulation in Electrical Engineering With LabVIEW /MatLab

#### Research work:

### \*\* Paper in National Conference:

- 1."Realization of oscillating voltage due to change in flux position from a d.c source" In JNU,New Delhi
- 2. "Realization of alternating triangular voltage waveform due to change in position of core from a d.c supply" In Bhewani ,Hariyana
- 3. "Green Chemistry and Green Engineering in Sustainable Development A Review" in RGGI, Meerut

### \*\* Paper in International Conference:

1."Realization of bidirectional response from a part of magnetic circuit due to d.c excitation "in Dehradun, Uttrakhand

#### \*\* Publications:

- 1. **Ashok Kumar Rajput**, Rajesh Kumar Tewari & Atul Sharma,"Realization of rectangular bidirectional pulses due to D.C excitation" International Journal of Electrical Engineering and Technology (IJEET), ISSN 0976 6545(Print),ISSN 0976 6553(Online) Volume 3, Issue 2, July- September (2012)
- 2. **Ashok Kumar Rajput**,"Visualization Base Simulation of D.C Network Theorems in Basic Electrical Engineering with LabVIEW"International Journal of Engineering and Advanced Technology (IJEAT) ISSN: 2249 8958, Volume-2, Issue-1, October 2012
- 3. **Ashok Kumar Rajput** ,Rajesh Kumar Tewari and Atul Sharma, "Utility Base Estimated Solar Radiation at Destination Pune, Maharashtra, India" Int. J. Pure Appl. Sci. Technol., Volume 13, Issue 1, pp. 19-26, Nov.(2012)
- 4. **Ashok Kumar Rajput**, "Simulation of R-L-C Series and Parallel Resonance in Basic Electrical Engineering with LabVIEW" Research Journal of Engineering Sciences ISSN 2278 9472, Volume-2, Issue-1,pp. 45-49, January (2013)

#### PLACE: Meerut

**DATE:11-03-2012** 

(A.K.Rajput)