CURRICULUM VITAE

NOSINA.KRISHNA CHAITANYA E-Mail: nosinakc@gmail.com

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Objective: Seeking a challenging and dynamic career in teaching field where my abilities can be of great use and where they can be exploited for the institute's good.

Educational Qualifications:

Course	Name of the Institution / University	Passed Out (Year)	% of Marks	Division
SSC	C.V.Raman high School(TM), Nellore	1996	75.67	Distinction
Inter(MPC)	RATNAM Junior College(EM), Nellore	1998	73.9	Distinction
B.Tech.(ECE)	PBR VITS, Kavali (JNTU,Hyd)	2002	74.62	Distinction
M.Tech.(DSCE)	J.N.T.University, Hyderabad	2009	83.9	Distinction

Teaching Experience: 9 ½ Years

S.No.	Organization	Post held	Duration	Total period
1	PBR Visvodaya Institute of Technology & Science, Kavali, Nellore(Dt.),AP	Assistant Professor (Ratified- May 2005)	21 th July 2003 to May 2010	~ 7 Y
2	PBR Visvodaya Institute of Technology & Science, Kavali, Nellore(Dt.),AP	Associate Professor (Ratified- June 2010)	April 2010 to Till date	~ 2 ½ Y

Workshops/Refresher Course Attended:

- 21 Days UGC Sponsored Refresher Course on "*Network Programming*" and obtained grade 'A' from 29.10.2003 to 18.11.2003, conducted by *JNTUH*, Hyderabad.
- Six day Training programme on "Instructional Design and Delivery" from 21.06.2004 to 26.06.2004, conducted by National Institute of Technical Teachers Training and Research (NITTTR), Chennai.
- Two day National level workshop on "*Emerging Trends in Embedded Systems*" from 23.03.2011 to 24.03.2011, conducted by *Narayana Engineering College*, Nellore.
- Two day National level workshop on "VLSI Design & Testing" from 02.12.2011 to 03.12.2011, conducted by PBR Visvodaya Institute of Technology & Science, Kayali.
- Five day Mission10x workshop on "High Impact Teaching Skills" from 31.10.2011 to 04.11.2011, conducted by Wipro with ISTE.

Conferences Presented/Participated:

- Participated in Two day International Conference on "RF & Signal Processing Systems(RSPS-2008)", from 01.02.2008 to 02.02.2008, organized by Koneru Lakshmaiah College of Engineering, Guntur.
- Presented a paper in one day National Conference on "Histogram Equalization Based Edge Detection of a Color YUV Image", at Siddartha Institute of Science & Technology", Puttur.
- Presented a paper in International conference on "Modified Efficient Routing Algorithm in Networks", at GNIT, Hyderabad.

National/International Journals Publications:

1. Confirmed a paper publication in International Journal on "Modified Efficient Routing Algorithm in Networks" in IJCSEITR.

Extracurricular activities:

- ECE Students Association Incharge
- NBA Coordinator
- Projects Coordinator(U.G & P.G)
- Class Incharge for the batch of 2008-12

- Discipline committee member in the Department
- EDC lab Incharge.

Projects:

M.Tech Project Title: Performance Evaluation of Digital Modulation Techniques With

and Without Power Control using MATLAB

Place: J.N.T.U.H, HYD

Subjects taught:

- Computer Networks
- Wireless Communications and Networks
- Electronic Devices and Circuits
- Analog Communications
- Pulse and Digital Circuits
- Basic Electronics
- Communication Theory
- Hardware/Software Co-Design (M.Tech)

Personal Details:

Father's Name: N.Seshaiah

Mother's Name: N.Narayanamma

Date of Birth : 29.08.1981

Nationality: Indian

Marital Status: Married

Declaration

I hereby informed that the information given above is true to the best of my knowledge.

Date:16.05.2012 N.Krishna Chaitanya