

## *Curriculum Vitae*



## *Identification*

- Name: Mohsen Sadeghi
- Nationality: Iranian
- Date of birth: 1987
- Marital Status: Single
- Race: Persian
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## *Education*

- Bachelor: Electronic and Computer Engineering Department, Shahrood University of Technology, Shahrood, Semnan, Iran, July 2011.
- Thesis Title: Designing and Fabrication of an Intelligent and Programmable clock and calendar.
- Supervisor: Dr. Soleimani
- Consulting Supervisor: Dr. Geraylou
- Grade: 4.81 out of 5
- Master: Electronic and Computer Engineering Department, Sadjad University, Mashhad, Iran, September 2013
- Thesis Title: Designing and Simulation of Optima Digital Unsigned Multiplier.
- Supervisor: Dr. Golmakani

- Consulting Supervisor: Dr. Nabovati, Dr. Ahadi Akhlaghi
- Grade: 5 out of 5

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## *Publication*

- 1- Mohsen Sadeghi, Jamal Rajabi, Abbas Golmakani, Novel 1-Bit Full-Adder Cell with Ultra-Low Delay, PDP and EDP, International Journal of Management, Information Technology and Engineering Vol. 1, No. 1 (Oct. 2013).
- 2- Mohsen Sadeghi, Aref Vakili, Abbas Golmakani, New Structure Precise Full-Wave Rectifier in CMOS Technology by Double-Ended Fully-Differential Circuit, International Journal of Nano systems Vol. 5, No. 1 (2012).
- 3- Aref Vakili, Mohsen Sadeghi, Abbas Golmakani, A Modern Precise Full-Wave Rectifier in CMOS Technology for Various Applications, Advanced Engineering Technology and Application-An International Journal Vol. 2, No. 1 (Jan. 2013).
- 4- Aref Vakili, Mohsen Sadeghi, Abbas Golmakani, New Structure of CMOS Full Wave Rectifier by Operational Amplifier, International Journal of Computer Sciences, Software Engineering and Electrical Communication Engineering Vol. 5, No. 1 (June. 2012).
- 5- Mohsen Sadeghi, Aref Vakili, Abbas Golmakani, Two Novel Methods for Designing Low-Power Half-Adder with Minimum Transistors, International Journal of Computing and Digital Systems, Accepted, Forthcoming Paper 2013.(in press)
- 6- Aref Vakili, Mohsen Sadeghi, Abbas Golmakani, A novel CMOS precise Full-Wave Rectifier by Operational Amplifier and new structure for Diode, International Journal of Management, Information Technology and Engineering Vol. 1, No. 2 (November. 2013).

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## *Articles on Refereed*

- 1- Mohsen Sadeghi, Jamal Rajabi, Abbas Golmakani, Implementation of Novel Ultra-Low Power and High-Speed 1-Bit Full Adder Cell, Amirkabir International Journal of Electrical & Electronic, Submitted 2013-09-23.

- 2- Mohsen Sadeghi, Abbas Golmakani, Low Power and High Performance Multiplier Based on an Improved Structure, Iranian Journal of Science and Technology Transactions of Electrical Engineering, Submitted 2013-05-22.
- 3- Mohsen Sadeghi, Maaruf Ali, Ultra-High Speed Low Power Multiplier Based on a Novel Lowest Transistor Count Structure, IEEE letter, Submitted 2013.
- 4- Mohsen Sadeghi, Jamal Rajabi, Abbas Golmakani, Low power and high efficient Multiplier based on a new structure, Turkish journal, Submitted 2013-10-6.
- 5- Mohsen Sadeghi, Jamal Rajabi, Abbas Golmakani, A novel low-power 1-Bit CMOS full-adder cell based on multiplexer, Turkish journal, Submitted 2013-11-20.
- 6- Mohsen Sadeghi, Abbas Golmakani, Low-Power and High Performance Full-Adder Cell, Iranian Journal of Electrical and Computer Engineering, Submitted 2013-05-28.
- 7- Hossein Rasekh, Mahmood Ghasemi, Mohsen Sadeghi, Abbas Golmakani, Employing Positive Feedback Technique to Improve Sim-ultaneously UGBW and Gain for Op-Amps, International Journal of Management, Information Technology and Engineering, Submitted 2013-11-13.

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## *National Conferences*

- 1- Mohsen Sadeghi, Aref Vakili, Abbas Golmakani, A Novel 1-bit Full Adder based on Transmission Gate, Isfahan's Electrical Engineering National Conference(IEEC), (September. 2012).
- 2- Mohsen Sadeghi, Aref Vakili, Abbas Golmakani, New Full Adder with Ultra Low Propagation Delay based on GDI, Isfahan's Electrical Engineering National Conference(IEEC), (September. 2012).
- 3- Mohsen Sadeghi, Aref Vakili, Abbas Golmakani, New Full Adder with Very Low Propagation Delay and EDP, Isfahan's Electrical Engineering National Conference(IEEC), (September. 2012).

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## *Professional & Scientific Membership*

- 1- Member of National Scientific Student's Organization of Electrical Engineering
- 2- Member of Iranian Solar Energy Scientific Society
- 3- Member of International Association of Engineers(IAENG), Member Number:135820

- 4- Reviewer in Circuits, Systems, and Signal Processing(Springer)
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### *Other Interests*

- 5- Software: Simulink by Matlab, Programming by Matlab, Xilinx ISE, Proteus, Pspice, Hspice, Verilog, Protel DXP, Codevision, Bascom, Genetic Algorithm.
- 6- Programming Languages: C, Pascal, FORTRAN, VHDL, and Assembly.
- 7- Research Interests: Digital and Analog Circuit Design, Digital Integrated Circuit based on CMOS (VLSI), Analog Integrated Circuit based on CMOS (IC), Optimization(Genetic Algorithm).
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