

- April 2002 to JUNE 2007, Assistant Professor and Head /EIE, Arunai Engineering college, Tiruvannamalai.
- April 2001 to April 2002, Lecturer/EEE, Vellammal college Engineering, Chennai.
- Sep 1998 to April 2001, Lecturer/EEE, Arunai Engineering college, Tiruvannamalai.
- Sep 1997 to Sep1998, Graduate Trainee, TNEB, Tiruvannamalai.
- April 1996 to August 1997, Lecturer/EEE, Arunai Engineering college, Tiruvannamalai.

DETAILS OF M.E. / M.TECH. DISSERTATION SUPERVISED

S.No.	Title of Dissertations	Year of Award	Name of Student
1.	DESIGN OF VOLTAGE AND FREQUENCY CONTROL OF SEIG FOR STAND ALONE RENEWABLE ENERGY GENERATION SOURCES	2011	Mr. sasikumar
2	ANALYSIS OF POSITIONING OF INDUCTIVE TYPE SFCL FOR SMARTGRID APPLICATION USING SIMULINK	2012	Mr.S.C.VIJAYAKUMAR
3.	A SINGLE-PHASE GRID CONNECTED PHOTOVOLTAIC SYSTEM WITH FACTS DEVICES FOR POWER FACTOR CORRECTION	2012	Mr. R. Sitharthan

Assist with Supervision/co-supervision of Masters and/or Doctoral students (during Post-doctoral Research period 2012-2013 at TUT, Pretoria, South Africa)

No.	Name	Student no.	Qualification	Supervisor/Co-supervisor other than Fellow	Title of Dissertation
1.	BENNY KGOHLWANE MAKOLA	206167041	M Tech Electrical Engineering	Prof JL Munda Dr.N. Mbuli	CO-ORDINATION OF TRANSFORMER TAP-CHANGERS IN A TRANSMISSION NETWORK
2.	AYUB WANJALA	212492884	M Tech Electrical Engineering	Prof JL Munda	Impact of Distributed Generation on Power network operation
3	Irah Xolisile Mhlambi	201068916	M Tech Electrical Engineering	Prof JL Munda	Reactive Power Management in Energy Distributed Systems

DETAILS OF B.E / B.TECH. DISSERTATION SUPERVISED

S.No.	Title of Dissertations	Year of Award	Name of Students
1.	NIOS2 Processor based airtomized soft MCO for a web server implementation on FPGA	2011	A.Mohamed Tamheed C.Manikandan S.Satheesh Kumar J.Sarath Kumar Vinoth kumar

2	Adolf Aerodynamic operations leverage in FGPA	2011	Akash Ravindranath S.Aravindan S.Laksshman I.Muhammed Suhail
---	---	------	---

Skill Set:

- Knowledge of FPGA and ASIC design flow.
- RTL, Test bench coding using HDL's.
- Knowledge of Static Timing Analysis.
- Knowledge of DFT methodology.
- Knowledge of CMOS integrated circuit design.
- Knowledge of Layout Development.
- Knowledge of DRC , LVS Verification and Parasitic Extraction.
- Knowledge of Matlab

Tools:

- **Xilinx**
ISE Project Navigator 6.1i.
- **Mentor Graphics**
 - ModelSim, Leonardo Spectrum.
 - IC Station, Calibre, Calibre xRC.
 - Eldo, Design Architect IC.
 - DFTAdvisor, FastScan.
- **Magma**
Blast Create, Blast Fusion.
- MATLAB & SIMULINK
- SPICE

Languages: : VERILOG, VHDL, C.

Platforms Used : MS DOS's Solaris, Windows NT/98/00/XP.

Projects & Professional Experience**ASIC FLOW :**

DESIGN and LAYOUT OF STANDARD CELL LIBRARY:

Description: The aim of the project is to design standard cells such as INVERTER, NAND, AOI functions for 1X ,2X and 4x drive strengths with equal RISE and FALL delays, using the DESIGN ARCHITECT for Schematic drawing, ELDO for simulation, IC Station for Drawing Layouts and CALIBRE for DRC, LVS and PEX.

DESIGN and LAYOUT OF 1-Bit FULL ADDER CIRCUIT :

Description: The aim of the project is to design a 1-bit full adder circuit in VHDL, using DESIGN ARCHITECT for Schematic drawing, ELDO for simulation, IC Station for Drawing Layouts and CALIBRE for DRC, LVS and PEX.

MAGMA FLOW:

MIPS Processor Design:

Description: At 0.13um technology miniMips processor was designed in MAGMA`s netlist-to-GDSII flow. Processor was designed to work at 200MHz with cell count 20k. The design was free from DRC and LVS violations, achieving the given time spec

FPGA FLOW:

DESIGN AND IMPLEMENTATION OF 4-BIT ALU USING VHDL AND LEO.SPEC. ON SPARTAN 3 FPGA:

Description: The Project involves thorough study of ALU circuit, writing synthesizable VHDL code for arithmetic functions such as ADDITION, SUBTRACTION, MULTIPLICATION and SHIFT operations and for logical operations such as AND, OR, XOR and NOT, and then writing test bench, and finally implementing the design on Spartan 3 with timing and area constraints.

RTL CODES:

MIPS Processor Design:

Description: The main objective of this project is to understand the modern Computer Architecture of RISC structure and to code, and implement it on a VIRTEX FPGA. . In this processor we will fetch the instructions from the instruction memory, decodes and generates control signals to perform the required operation. For this we designed three memory units for data, register banks and instruction memory. It can handle arithmetic, logical, load, store, and branching operations.

Language & Tools Used :Mentor graphics ModelSim, Leonardo spectrum
Xilinx ISE, Verilog.

Design of a 5 stage pipelined mini RISC Processor:

Description: The design and simulation of a Mini RISC processor based on the MIPS ISA is proposed in this project. The proposed processor ISA comprises a total of 32 instructions, including arithmetic, logical and shift operations on integer data operands. The design activity involves capturing and synthesizing the defined specifications and targeting the same to XC2V2000 (Xilinx).

Language & Tools Used :Mentor graphics ModelSim, Leonardo spectrum
Xilinx ISE, Verilog.

Design of UART Transmitter.

Description:This project involves the development of an UART transmitter with a **baud rate** of **19200bps**. The transmitted data will be of 10 bit which includes 8 bit data and start and stop bit of

one bit each. The coding was done in VERILOG and downloaded(fused and tested) on to XILINX SPARTAN-3 Prototype board, with the keyboard interfaced through PS2 port. A test bench was written for testing the device.

Language & Tools Used :Mentor graphics ModelSim, Leonardo spectrum
Xilinx ISE, Verilog.

Design of Digital Alarm Clock.

Description:A Digital Alarm Clock, which can display current time, load new time, set alarm time and sound alarm, was implemented in **VERILOG HDL**. A test bench was written and then functionality verified. The design was synthesized, floor-planned placed and routed. The bit file generated, was downloaded on to the XILINX demo board

Language & Tools Used :Mentor graphics ModelSim, Leonardo spectrum
Xilinx ISE, VHDL.

Design of Car Counter

Description:The project involves the development of a device to count the number of cars in a parking lot. The lot has a gate through which only one car at a time may enter or leave. The coding was done in VHDL and fused and tested on XILINX SPARTAN-3 Prototype board.

Language & Tools Used :Mentor graphics ModelSim, Leonardo spectrum
Xilinx ISE, VHDL.

Refereed conference and Workshop Publications

National conference

1. A.Senthil Kumar, T.Koteswaran, **Achieving improved Bandwidth utilization in a token Passing field bus network**, 5th National conference on Intelligent systems, Automation and Signal Processing (NCIASP-03), organised by IEEE Chennai chapter, March 1-2, 2003. (CDROM)
2. A.Senthil Kumar, T.Koteswaran, **Smart Sensor Technology**,5th National conference on Intelligent systems, Automation and Signal Processing (NCIASP-03), organised by IEEE Chennai chapter, March 1-2, 2003. (CDROM)
3. A.Senthil Kumar, M.Arumugam, **Component Based Embedded system**, 4th National conference on automated and instrumentation control (ACI-05), organised by ISA ,Chennai chapter, Tamil Nadu, February 3-4, 2005. (CDROM)
4. A.Senthil Kumar, NaliniKanth, **Double inverter fed vector control induction motor**,1st National conference on Cutting Edge Technologies in Power Conversion and Industrial Drives (PCID-05), BAIT, Erode, Tamil nadu, India, March 25-26, 2005. (CDROM)
5. A.Senthil Kumar, P.Suresh Kumar, **Equalisation concept for Edge**, 7th National conference on computer communication & mobile computing system (NCCC-MCS-05), GCT,Tirunelveli, Tamil Nadu, April 4th, 2005. (CDROM)

6. A.Senthil Kumar, M.Arumugam, **New approach for Embedded system**, 5th National conference on intelligent computing in communication & autoamtion (NCICCA-05), AKCE, Tamil Nadu, India, December 23-25, 2006. (CDROM)
7. Senthil Kumar, A., and R.Sitharthan., **"A Single-Phase Grid Connected Photo Voltaic System With Facts Devices for Power Factor Correction"** 1st National conference on Advances in Electrical Engineering (NCAEEE-2012), SVCE, Sriperumbuour, Chennai, India, Feb.17-18, 2012, pp.247-251.
8. L. Ajay, D. Rajareddy, N. Chitra and A. Senthil Kumar., **'Modeling of a Synchronous Generator Supplying RLC Load in a Microgrid,'** 2nd National Conference on Expanding Horizon in Computer, Information Technology, Telecommunication, Electrical and Electronics (EXCITE-2013), CIT, Coimbatore, India, April 2-3, 2013 (ISBN 978-0-9888421-8-2)

International Conference

1. A.Senthil kumar, M.Arumugam, Samleo L. Joseph, **Aristo Robot**, 4th International conference on Human Machine interface (ICHMI-2004), organised by Touch Lab, MIT, held at IISC Bangalore, December 20-23, 2004.(CDROM)
2. Senthil Kumar, A., Singh, G. K., and Saini, **"Capacitive Excitation of a Six Phase Self Excited Induction Generator-An Experimental Result"** International Conference on Energy Engineering-2009 (ICEE-2009), January 7-9, 2009, Pondicherry Engineering college, Pondicherry, India; Paper No. EEE 316 (CD-ROM).
3. Senthil Kumar, A., Singh, G. K., and Saini, **"Steady State Analysis of Six-Phase Self-Excited Induction Generator using Fmincon Technique"** IEEE Conf. on Electrical Energy Systems and Power Electronics in Emerging Economics, (ICEESPEEE2009), SRM University, Chennai, April 16-17, 2009, pp.303-308
4. Senthil Kumar, A., Singh, G. K., and Saini, **"Steady-State Analysis of a Self-Excited Six-Phase Induction Generator for Alternate Renewable Energy Generation"** 3rd IEEE Conf. on Power Electronics Systems and Applications -2009 (PESA-09), Hong Kong Polytechnique University, Hung Hom, Hong Kong, May 20-22, 2009, pp 1-5.
5. Senthil Kumar, A., Singh, G. K., and Saini, **"Optimization-Based Steady State Analysis of Six-Phase Self-Excited Induction Generator"** International Conference on Operations Research applications in Engineering and Management (ICOREM-09), Anna University, Trichy, India, May 27-29, 2009, pp.2744-2748
6. Senthil Kumar, A., Singh, G. K., and Saini, R. P., **'Selection of Capacitor for Self-Excited Six-Phase Induction Generator'**, 3rd IEEE Int. Conf. on Power System (ICPS-2009), IIT Kharagpur, India, Dec. 27-29, 2009, pp 1-6.
7. Senthil Kumar, A. and Singh, G. K., **'Performance Analysis of Self-Regulated and Self-Excited Six-Phase Induction Generator'**, 2nd IET International Conference on Sustainable Energy and Intelligent System (SEISCON 2011), Chennai, INDIA, July 20-22, 2011, pp. 480-484.
8. Senthil Kumar, A. and Sathiyarayanan, J.S., **'Power Electronics application for Wind Power System'**, 3rd International Conference on Science Engineering and Technology (SET 2011), VIT university, Vellore, India, Nov.17-18, 2011, pp. 213-224.

9. Senthil Kumar, A. and Elango.T., '**Recent Advances in Power Electronics and Digital Control Strategies for Wind Energy Conversion System: A Review**', International Conference on Renewable Energy Technologies(iCORET 2011), PSG College of Technology, Coimbatore,India, Dec. 16-17, 2011, pp.124-128.
- 10 Senthil Kumar, A. and Sathiyarayanan, J.S., '**Maximization battery lifetime and improving efficiency**', IEEE Int. Conference on Devices, Circuits and Systems (ICDCS'2012), Karunya university, Coimbatore, india, March 15-16, 2012,pp. 603 - 606
11. Senthil Kumar, A. and Vijayakumar.S.C., '**Analysis of the Positioning of Super conducting Fault Current Limiters for the Smart Grid Application using MATLAB**', International Conference on Engineering and Technology (ICET2K12), Sri Venkateswara College of Engineering and Technology, Chennai, India, April 18-19, 2012, pp.472-476.
12. K.Prabaakaran, N.Chitra and A.Senthil Kumar, '**Power Quality Enhancement in Microgrid – A Survey**,' IEEE International Conference on Circuits, Power and Computing Technologies [ICCPCT-2013], Noorul Islam University, Kumaracoil, Kanyakumari, India, March 21-22,2013, pp.126-131
13. Senthil Kumar, A., Josiah L Munda and Singh, G. K., '**Steady-state and dynamic load Performance of Six -Phase Self-Excited Induction Generator**', International Conference on Integrated Waste Management and Green Energy Engineering (ICIWMGEE'2013) , Johannesburg, South Africa, April 15-16, 2013,pp.292-296. (ISBN: 978-93-82242-26-0)
14. Chitra.N, Logeshwari.V,A.Senthil Kumar and Josiah L Munda., '**Optimal Power-Sharing of Multiple Distributed Generators in a Microgrid System**,' Fourth International Conference on Control, Communication and Power Engineering 2013(CCPE 2013), Bangalore, India, April 26-27,2013,pp 132-141.

Research Paper Published in International Journal:

- (i). Senthil Kumar, A., Singh, G. K., and Saini, R. P., '**Performance evaluation of series compensated self-excited six-phase induction generator for stand-alone renewable energy generation**', Energy,(Elsevier publications) vol. 35, issue.1, pp. 288-297, January 2010. (impact factor 1.72)
- (ii). Senthil Kumar, A., Singh, G. K., and Saini, R. P., '**Steady-state modeling and analysis of six-phase self-excited induction generator for renewable energy generation**', Electric Power Components and Systems, (Taylor and Franchis publisher)Volume 38, Issue 2,pp.137-151,January 2010. (impact factor 0.376)

- (iii). Senthil Kumar, A., Singh, G. K., Saini, R.P., **‘Selection of Capacitance for Self-Excited Six-Phase Induction Generator for Stand-Alone Renewable Energy Generation’**, Energy Journal, (Elsevier Publication), vol. 35, issue.8, pp. 3273-3283, August 2010. (impact factor 1.72)
- (iv). Senthil Kumar, A., Singh, G. K., and Saini, R. P., **‘A self-excited six-phase induction generator for stand-alone renewable energy generation’**, European Transactions on Electrical Power, volume 20, Issue 7, pp.884-900, October 2010. (impact factor 0.56)
- (v). Senthil Kumar, A., Singh, G. K., and Saini, R. P., **‘Performance Analysis of a Simple Shunt and Series Compensated Six-Phase Self-Excited Induction Generator for Stand-Alone Renewable Energy Generation’**, Energy Conservation and Management Journal, (Elsevier Publication) , volume 52, Issue 3, pp.1688-1699, March 2011. (impact factor 2.46)
- (vi). A.Senthil Kumar, N.Chitra and G.Tamizharasi., **‘Optimal Design of Microgrid in an Autonomous mode using Antcolony Optimisation,’** International Journal of Electronics Signals and Systems (IJESS), volume 2, Issue 1, pp.14-18, May 2012
- (vii) A.Senthil Kumar, J.S.Sathiyarayanan., **“Fuzzy Based Power Quality Improves with Doubly Fed Induction Generator –Based Wind Turbines”**, International Journal of Engineering Research and Development, volume 5, issue 6, pp.15-19, December 2012
- (viii) A.Senthil Kumar, Josiah L Munda, N.Chitra, K.Prabaakaran., **‘Ant Colony Optimization Adopting Control Strategies for Power Quality Enhancement in Autonomous Microgrid’**, International Journal of Computer Applications, volume 63, issue 13, pp 34-38, February 2013.
- (ix) J.S.Sathiyarayanan, A.Senthil Kumar., **‘Power Quality Improvement Wind Energy System Using Cascaded Multilevel Inverter,’** International Journal of Renewable Energy Development, Volume 2, issue1, pp 35-43, February 2013
- (x) A.Senthil Kumar, Josiah L Munda, S.Ravichandran., M.Arumugam., **‘Investigation of a Three Phase Five Level Voltage Source Inverter with reduced switch for Shunt Active Power Filter’**, Archives Des Sciences Journal (ISSN: 1661-464X), Volume. 66, Issue. 4, , pp 121-132, April 2013. (Impact factor 0.474).

- (xi) A.Senthil Kumar, Josiah L Munda, S.Ravichandran., M.Arumugam., ‘**A Novel Three Phase Five Level Voltage Source Inverter for Shunt Active Power Filter**’, Archives Des Sciences Journal (ISSN: 1661-464X), Volume. 66, Issue. 4, , pp 312-318, April 2013. (**Impact factor 0.474**).
- (xii) M. Sahithullah and A.Senthil Kumar., ‘**Power Quality Enhancement Strategy of Hybrid Distributed Generation System**’, International Journal of Electrical Engineering(ISSN 0974-2158), Volume 6, Number 2, pp.201-211, June 2013

Additional Information

Courses:

- **Diploma in Nano technology** at Nano Science and Technology Consortium, at New Delhi, July 2007
Duration: 6 month
- **PG Diploma in VLSI Design** at Sandeepani School of VLSI Design, Training division of CG-CoreEL Programmable Solutions Pvt. Ltd, Bangalore (**Authorized Training Centre for Xilinx, Mentor Graphics and MAGMA, USA**), January 2006.
Duration: 1 year
- **Diploma in Information System Management** at Aptech computer Education, Tiruvannamalai, April 1996.
Duration: 1 year

Number of Research Projects completed / ongoing from various funding sources

Name of the Funding Agency	Title of project	Total Amount	Period of support and offer letter no.	Completed/on-going
AICTE	MODROB On Power Electronics and Drives Lab	Rs. 5 Lakhs	One year (Ref. No.8024/RIFD/M OD-324/2010-11/Dated	Completed

			31.03.2011)	
AICTE	SDP- Embedded Systems and its Application to Power Electronics	Rs. 6.5 Lakhs	Two Weeks (File No. 1-78/RIFD/SDP(166)/2010-11 Dated 28.03.2011)	12.12.2011 to 23.12.2011 Completed
AICTE	RPS- FPGA based Power Conditioner for Fuel Cell in Stand-Alone Renewable Energy Generation	Rs. 13 Lakhs	Two Years (File No. 8023/RID/RPS-44/2011-12)	On-going

Award

S.No.	Details of the Award	Issued Organization	Award Type	Year
1.	Partial Travel Grant award to attend the conference PESA-09, Hong Kong	Council of Scientific & Industrial Research (CSIR), India	Cash Award Rs.14000/-	2009
2.	Best Lecturer Award	Arunai Engineering College	Cash award & Certificate	2004
3.	Best Faculty Award	Arunai Engineering College	Cash award & Certificate	2006
4.	Best Research Paper Award	IET Conferences (SEISCON 2011)	Award & Certificate	2011

Certificate

S.No.	Details of the Certificate	Rank	Issued Organization	year
1.	Analysis of Inverter (PG COURSE)	Second	SCE, Thanjavur	2000
2.	Advanced Semiconductor devices (PG COURSE)	Second	SCE, Thanjavur	2000

--	--	--	--	--

Membership of Technical Societies

Organization	Year of Induction	Grade of Membership(Life member)
ISTE (India)	2003	MISTE
IEI (India)	2005	Associate Member
CSI (India)	2007	Associate Member (Membership no: 00144803)
IAENG (Hong Kong)	2009	Member (Membership No: 102368)
IACSIT (Singapore)	2009	Member (Membership No: 80332238)
IAEST	2011	IAEST Senior Member (Membership no: 011084248)
Singapore Institute of Electronics(SIC)	2012	SIE Senior Member (Membership no: 80090001)
Institute of Doctors Engineers and Scientists(IDES & ACEEE)	2013	Honorary Senior Member (Membership no:1154)
Universal Association of Computer and Electronics Engineers(UACEE)	2013	Senior Member (Membership No: SNM1004144)

He has been conferred the "**BEST ACADEMIC RESEARCHER**" award by the Association of Scientists, Developers and Faculties (ASDF), an international non-profit organization focusing on research and education.

Invited lectures and Chairmanships at national and international conference/seminar/symposium

S.No	Title of lecture/keynote	Date & Venue	Title of the conference/seminar/symposium	Organized by	International/national/state
1.	Safety measures for EA	05.03.2011, Vellore	National Conference (chairmanship)	AHCET	National
2.	Micro grid and smart grid	08.04.2011, Chennai	National Conference – RET-2011(chairman)	VEC	State
3.	Smart Grid	09.09.2011, Vellore	NATIONAL LEVEL TECHNICAL SYMPOSIUM – ROCX-2011 (CHIEF GUEST)	GTECH	National
4.	Multi inverter	15.09.2011, Chennai	NATIONAL WORKSHOP ON HARDWARE IMPLEMENTATION OF PED (Special Talk for 2 sessions)	SRM university & ABB company	State
5.	Power	17.09.2011,	INAGURATION OF EEE	KEC	National

	electronics for RES	TVM	ASSOCIATION (CHIEF GUEST)		
6.	Smart Grid for RES	24.09.2011, Hosur	NATIONAL TECHNICAL SYMPOSIUM- FARADAY-2011 (CHIEF GUEST)	PMEC TECH	National
7.	Induction motor drive	02.12.2011 Chennai	Anna university sponsored Faculty development program on “SSD” Lecture for 2 sessions	Rajalakshmi Engineering College	State
8.	Matlab application Electrical Engineering	10.12.2011 Gudur, Andhra Pradesh	CSIR sponsored workshop of Matlab application in electrical engineering (Chief Guest for valediction and Special talk)	Audishankara College of Engineering & Technology, Gudur	National
9.	Embedded system application	02.03.2012 Hosur	National Conference on communication, computation and control system(NCCCC-2012) (Technical Judge and keynote address)	PMEC TECH Hosur	National
10.	Android Environment in Embedded System	07.03.2012 Vellore	National Level Technical Symposium (SEIZURE’12) (Chief Guest and Keynotes address)	Kingston Engineering College, Vellore	National
11.	Smart grid technology review	27.07.2012 Chennai	Guest lecture	Gojan school of business and technology	State
12.	Android development in mobiles	22.08.2012 Chennai	National Conference – NCONAEST’12 (Technical Judge and keynote address)	Magna college of Engineering, Chennai	National

Editorial Board Member

1. International Association of Scientific Innovation and Research (IASIR)
2. International Journal of Advanced Research in Electrical, Electronics and Instrumentation Engineering (IJAREEIE) -- <http://www.ijareeie.com/editorial-board>

Reviewer for International Journals

1. Electric Power Components and System (Taylor and Franchis Publisher)
2. International Journal of Computer and Electrical Engineering (IJCEE)
3. inderscience journal
4. International Association of Scientific Innovation and Research(IASIR)
<http://www.iasir.net/boardmembers.html>
5. International Journal of Engineering Research and Technology(IJERT)
<http://www.ijert.org/about-us/review-board>

Foreign country visited

Visited HONG KONG in MAY 2009

Visited Johannesburg in April 2013

Recognized Supervisor

He as the recognized “Research guide” for the Doctorate Programme and M.S by Research programs offered by the Anna University, Chennai (**Reference No. 15.399.01**) ,Anna university of technology Chennai, Chennai (**Reference No. AUT/Ph.D/2011(R)/EE002**) and Sathyabama University, Chennai (**Reference No. 31042**)

He is guiding the Ph.D scholars more 5 members from different university.

He also the responsibility for research interaction industry and organization is given below

Co-coordinator - Fuel Cell division (DST)

Co-coordinator - Research Project (TVS Brakes India Limited)

Technology sharing by VEC with Institute of Energy studies, Vietnam

Dr. A.Senthil Kumar is on the process to have tie up with Director, Institute of Energy studies, Vietnam for scientific and technological cooperation contents in the field of energy between India and Vietnam. This project deals with “**Studying the application possibility of Six-phase Self-Excited Induction Generator in conditions of Vietnam**”

References

1. Dr.G.K.Singh
Professor
Department of Electrical Engineering
Indian Institute of Technology Roorkee
Roorkee -247 667, India
2. Dr. R.P. Saini
Associate Professor
Alternate Hydro Energy Centre
Indian Institute of Technology Roorkee
Roorkee -247 667, India
3. Dr. S.P. Das
Professor
Department of Electrical Engineering
Indian Institute of Technology Kanpur
Kanpur – 208 016, (UP), India.
4. Prof. Josiah L Munda, DEng
Associate Dean, Faculty of Engineering and the Built Environment,
Tshwane University of Technology, Pretoria, SouthAfrica
Tel: +27 12 382 5120/5844

Fax: + 27 12 382 5568
Cell: +27 84 415 6692
Email: mundajl@tut.ac.za
Alternate Email: ljmunda@yahoo.com
www.fe.tut.ac.za

Personal Profile:

Name : Dr. A. Senthil Kumar
Date of Birth : 01.09.1975
Father's name : AJITHAN.M.
Marital Status : Married
Sex : Male.
Languages Known : English & Tamil
Nationality : Indian.
Passport Number : E7287393
Passport issue date : 28.11.2003
Passport Expiry date : 27.11.2013
Passport issue place : Chennai.
Permanent Address : 745/126, Min Nagar, Second Street,
Vengikkal, Tiruvannamalai, India
Pin – 606604
Communication Address : Dr.A.SENTHIL KUMAR.,
Postdoctoral Fellow
Tshwane University of Technology
Faculty of Engineering and the Built Environment
Electrical Engineering Department/CEEP
Building 6 room 275
Private Bag X680
Pretoria-0001
South Africa
Email id : vastham@gmail.com, senthilkumara@tut.ac.za
MOBILE NO : +27 743559011

Declaration:

I here by declare that all the information given above is true to the best of my knowledge and belief.

Place: Tiruvannamalai

yours truly


Dr. A. Senthil Kumar